孕龍科技股份有限公司
Zeroplus Technology Co., Ltd.

SPECIFICATION

MODEL: B09016-LAP-SCCB-M

PART NO： _____

VERSION： V1.01

Approver		Check	Design
GM	PM		

Customer Confirm

＊Please fax the file to Zeroplus Technology after signing.

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Content

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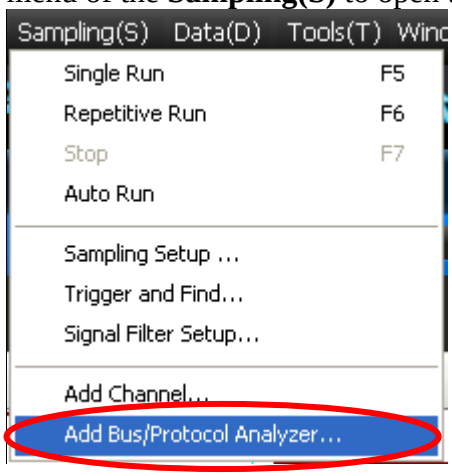
1 Software Register

Please register the software as the following steps:

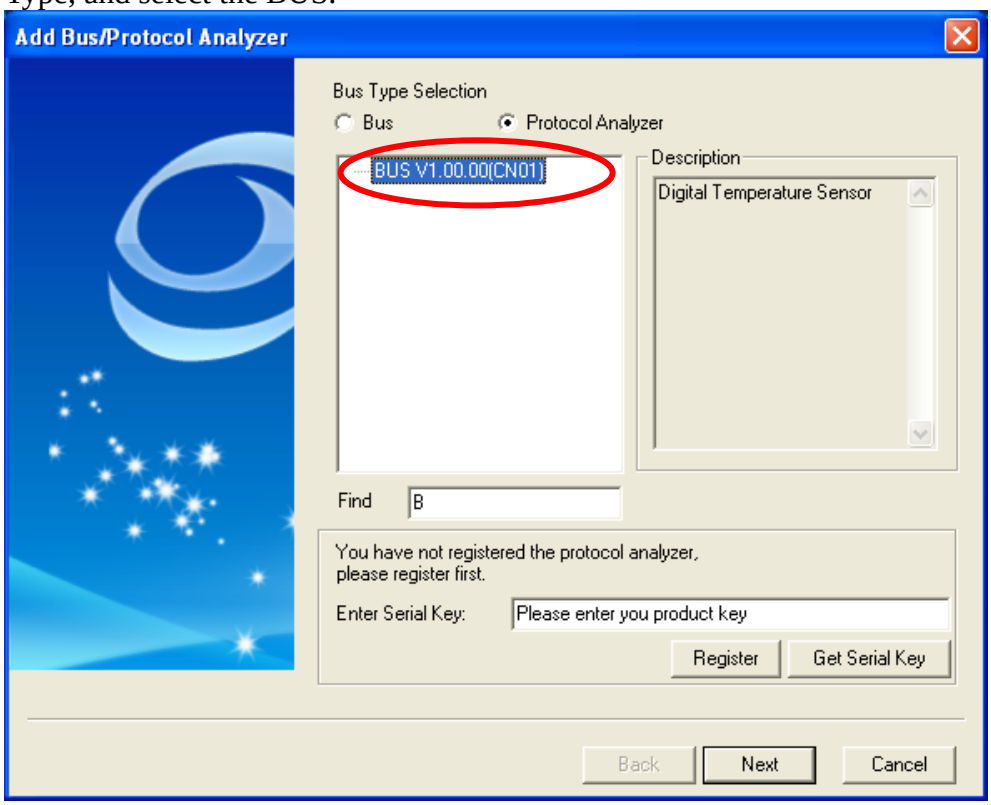
※ Remark1: The registration steps for all protocol analyzers are the same; you can complete the registration by following procedures. Following is an example on how to register the Protocol Analyzer BUS.

※ Remark2: We won't have additional notice for you, when there is any modification of the module specification. If there is some unconformity caused by the module version upgrade, users should take the module software as the standard.

STEP 1. Open the Logic Analyzer and select the **Add Bus/Protocol Analyzer** item on the pull-down menu of the **Sampling(S)** to open the **Add Bus/Protocol Analyzer** dialog box.



STEP 2. Select Protocol Analyzer item in the Add Bus/Protocol Analyzer dialog box, expand the Other Type, and select the BUS.



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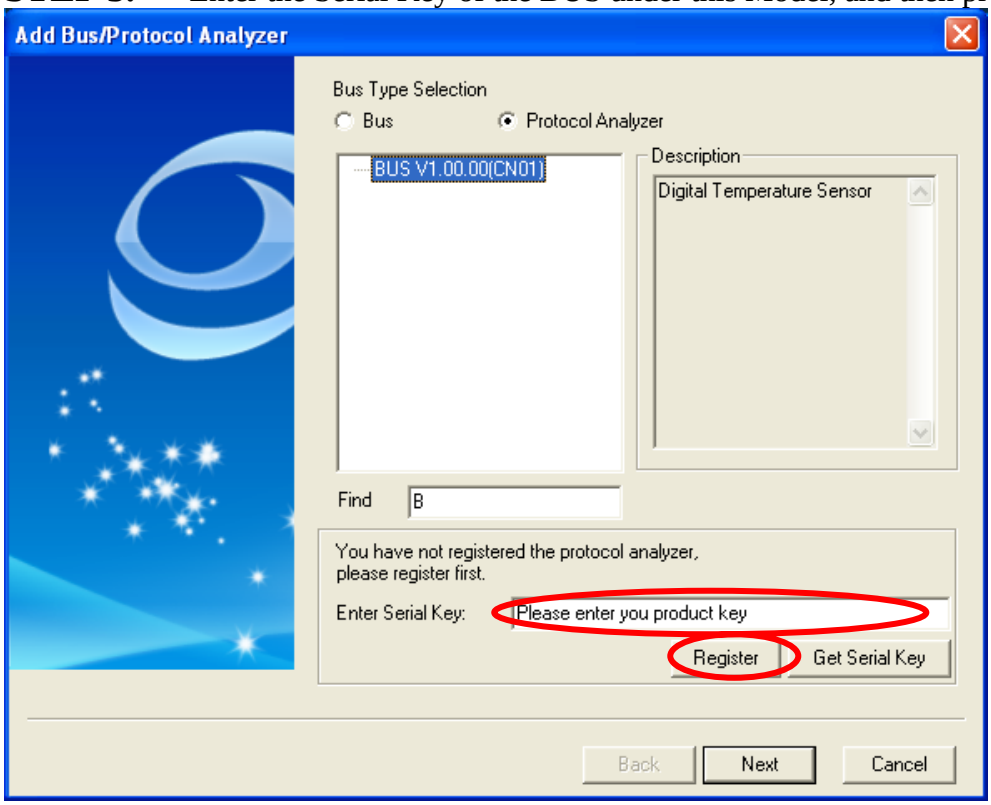
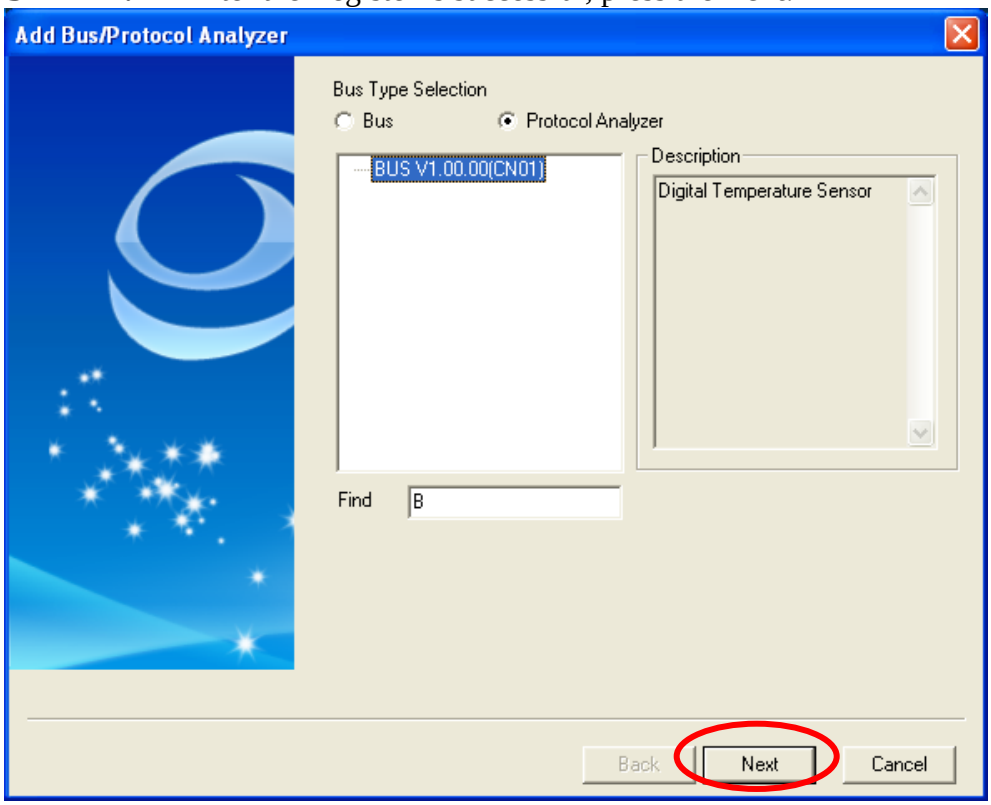
STEP 3. Enter the Serial Key of the BUS under this Model, and then press the **Register**.



STEP 4. After the Register is successful, press the **Next**.



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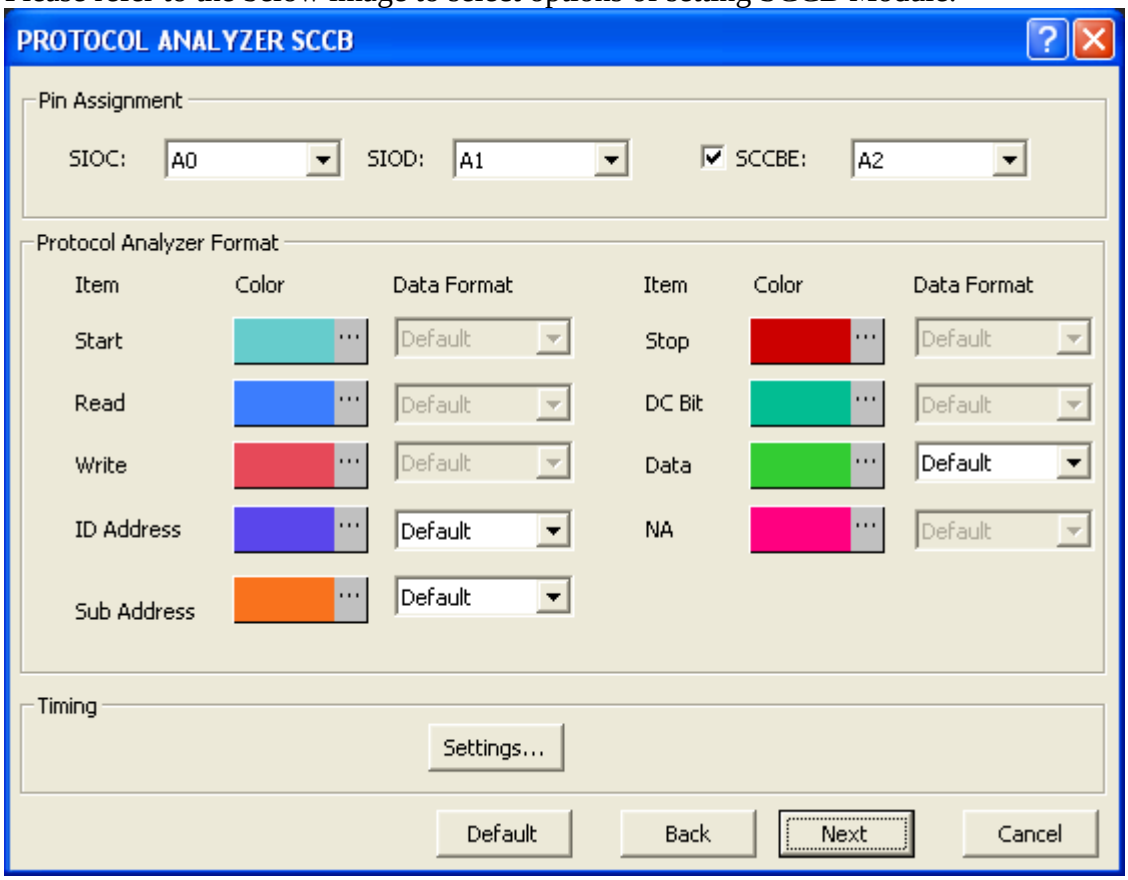
2 User Interface

Please refer to the below image to select options of setting SCCB Module.



Pin Assignment:
SIOC: It is the Clock Signal Channel.
SIOD: It is the Data Signal Channel.
SCCBE: It is the Chip Select Control Channel. It is effective in the Low Level. The default is to be selected. When not selecting this item, SCCBE, it means that the SCCBE is always in the Low Level.

Protocol Analyzer Format: Users can set the color of the packet as their requirements. The Items (ID Address, Sub Address, Data) can be set as Binary, Decimal, Hexadecimal, ASCII or Default. And the Data Format of the Items (WREN ID Address, Sub Address, Data) in the Waveform Display Area and Packet List is controlled by the Protocol Analyzer. The default Data Format is controlled by the main program and the Data Format of the Item is the Default.

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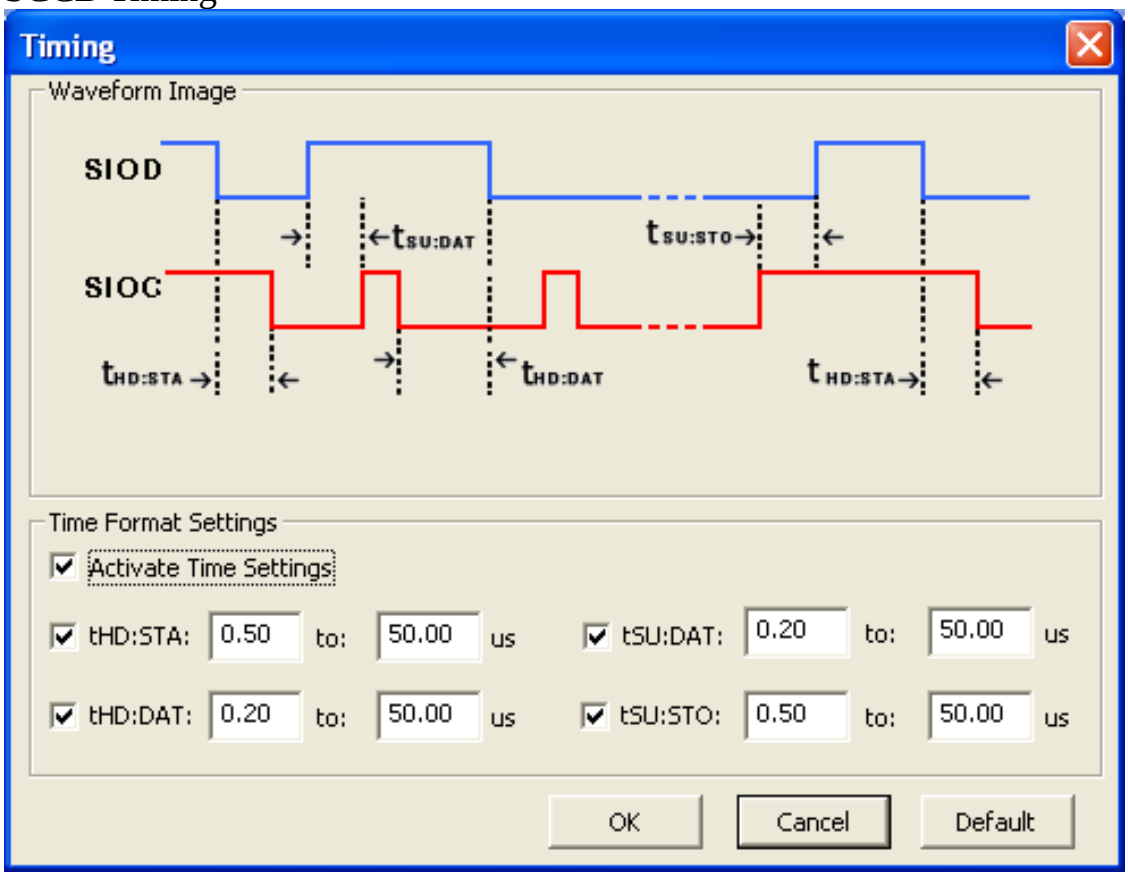
SCCB Timing



START Time Interval, tHD: STA, is from the Falling Edge of the SIOD to the Falling Edge of the SIOC; END Time Interval, tSU: STO, is from the Rising Edge of the SIOC to the Rising Edge of the SIOD; the Time Sequence of data can be divided into two parts. tSU: DAT is the Time Interval which is from the Rising Edge or Falling Edge of the SIOD to the Rising Edge of the SIOC; tHD: DAT is the Time Interval which is from the Falling Edge of the SIOC to the Rising Edge or Falling Edge of the SIOD.

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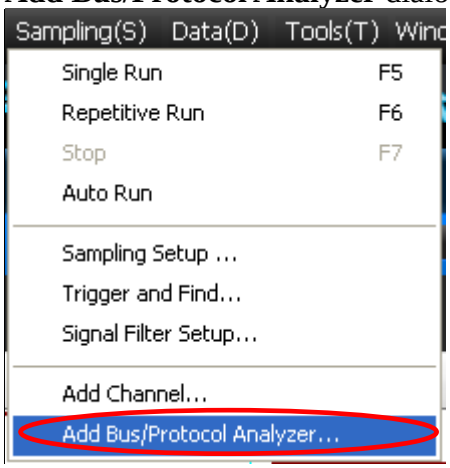
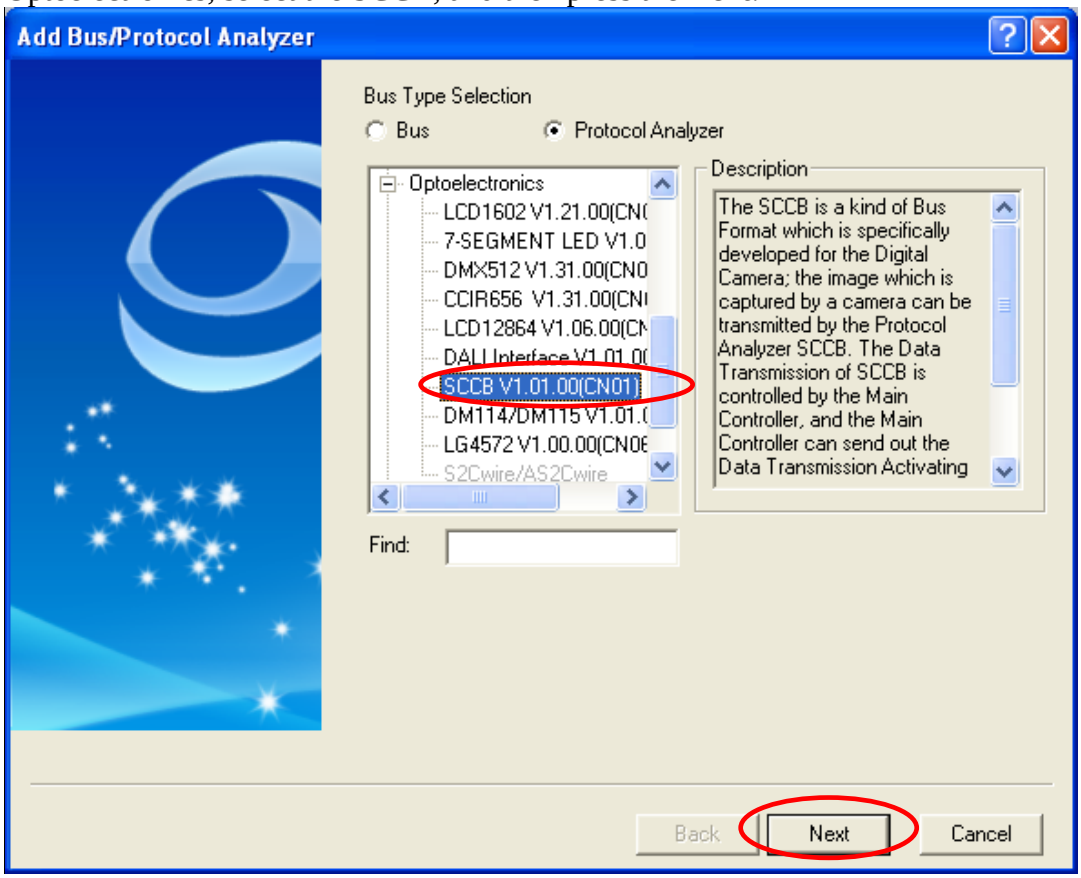
3. Operating Instructions

STEP 1. Select the **Add Bus/Protocol Analyzer** item on the pulldown menu of the **Sampling(S)** to open the **Add Bus/Protocol Analyzer** dialog box.



STEP 2. Select the Protocol Analyzer item in the Add Bus/Protocol Analyzer dialog box, expand the Optoelectronics, select the SCCB, and then press the **Next**.



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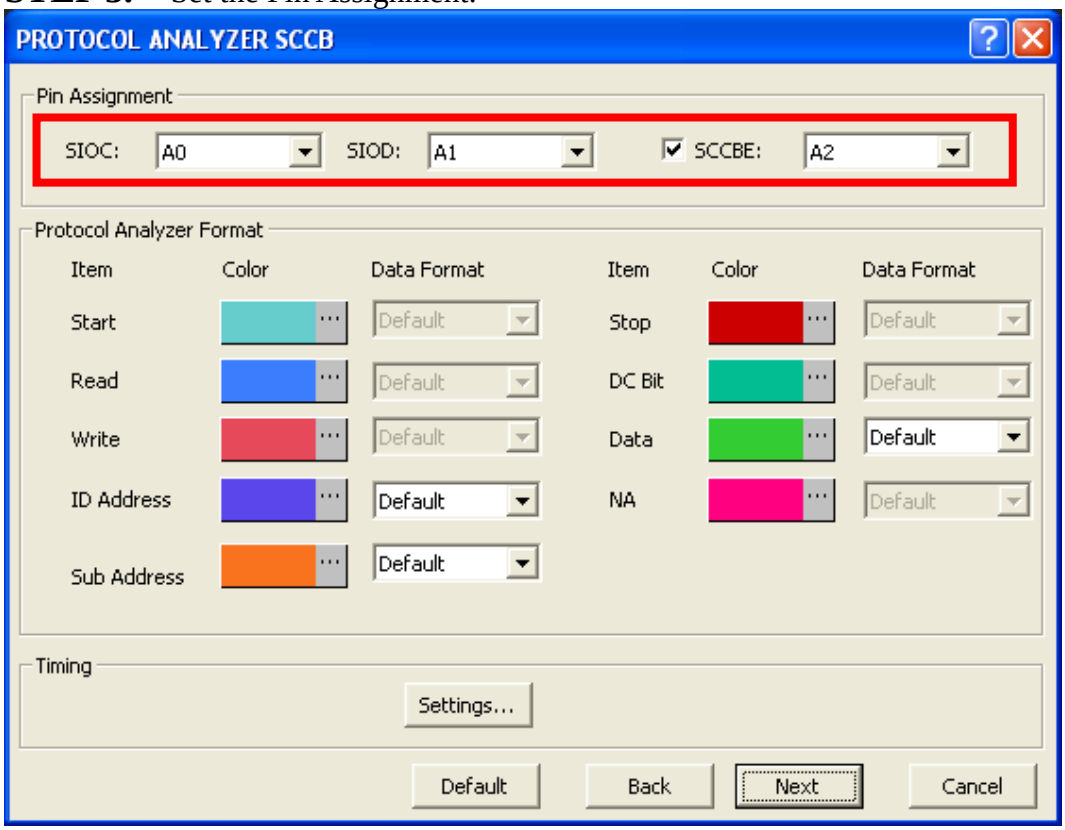
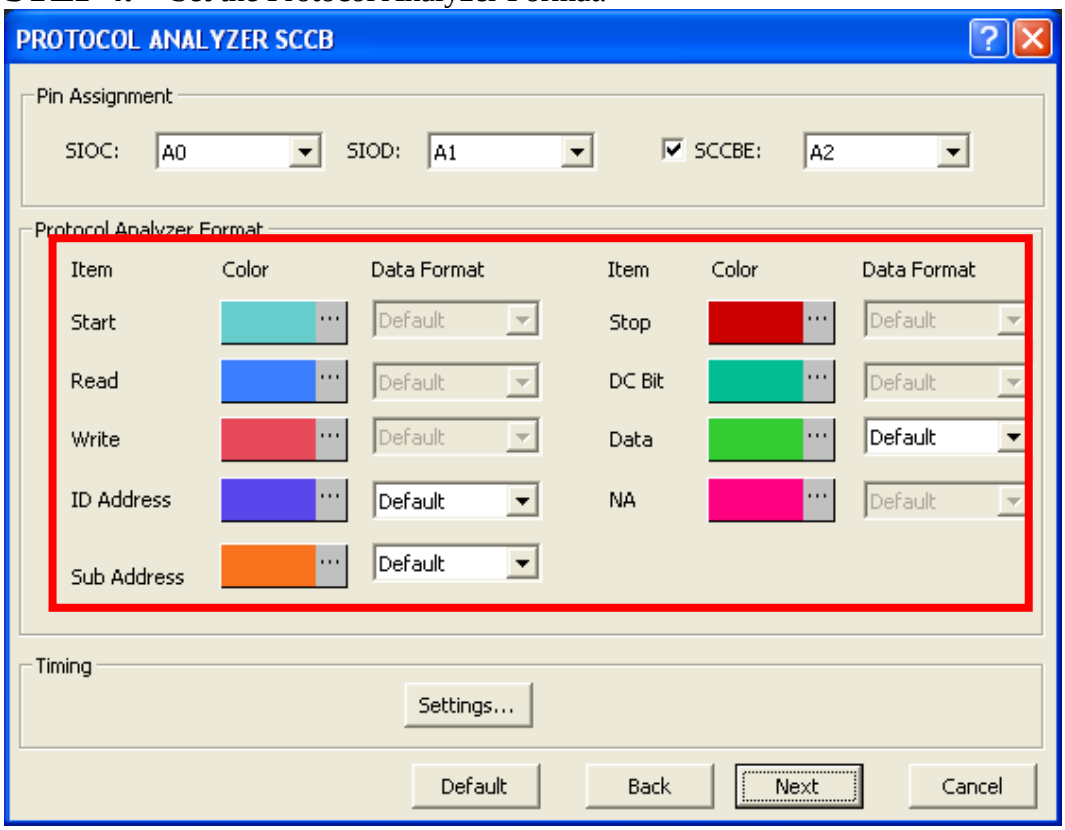
STEP 3. Set the Pin Assignment.



STEP 4. Set the Protocol Analyzer Format.



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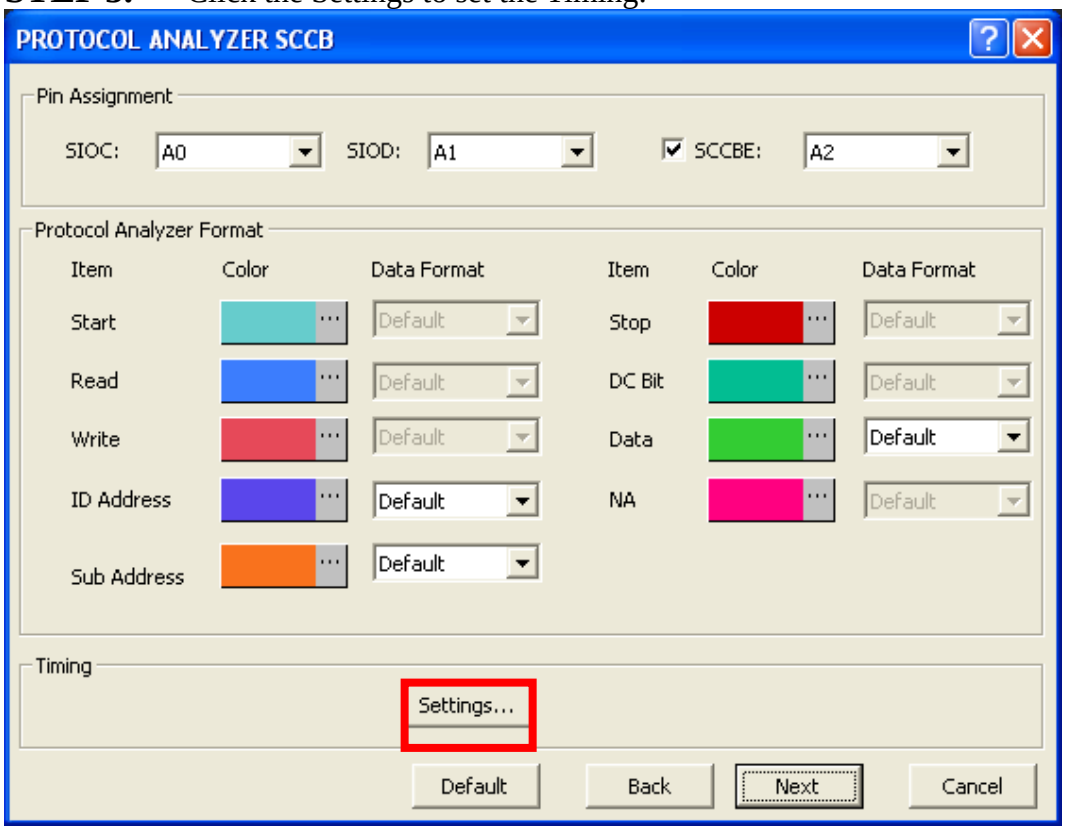
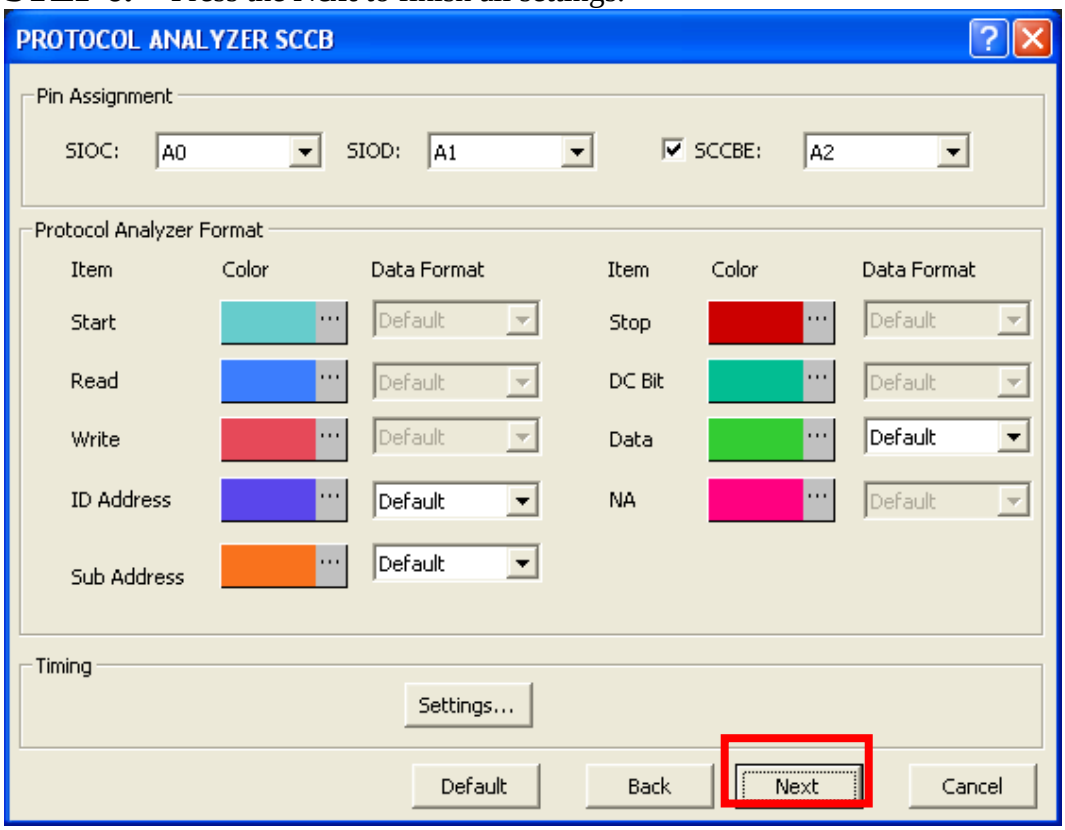
STEP 5. Click the Settings to set the Timing.



STEP 6. Press the **Next** to finish all settings.



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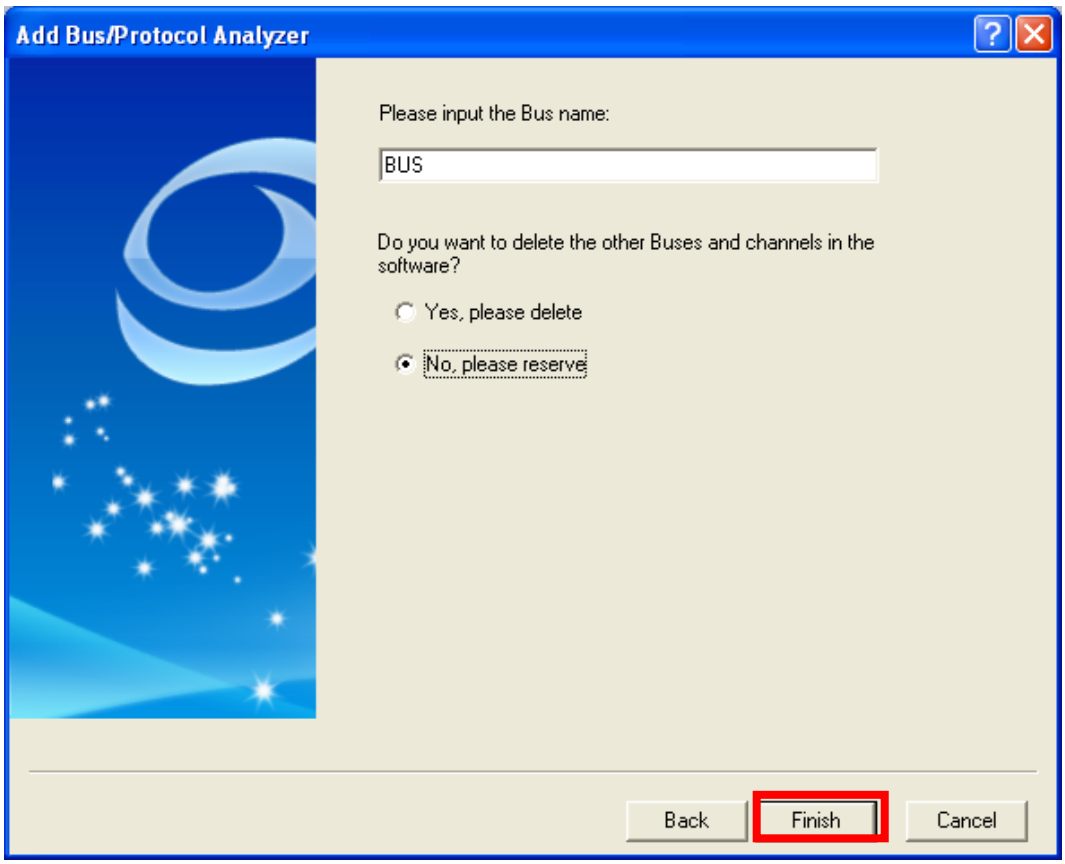
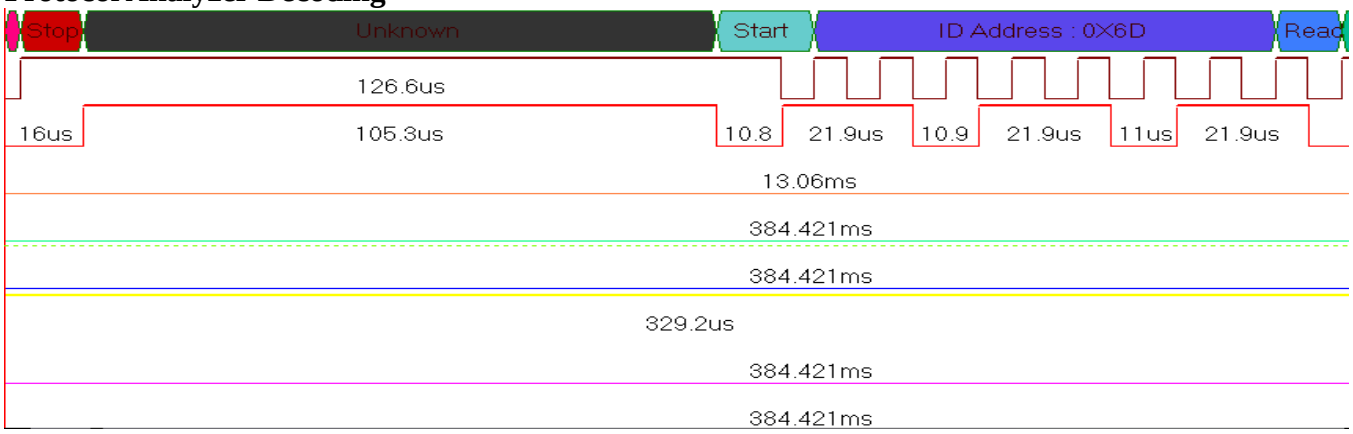
STEP 7. Please enter the Bus Name, select **Yes, please delete** or **No, please reserve** and then press **Finish**.



STEP 8. Following pictures show the completion of the protocol analyzer decoding and packet list. The trigger condition is set as Either Edge; the memory depth is 128K; the sampling frequency is 10MHz. (the sampling frequency should be more than 8 times higher than the signal to be tested.)

Protocol Analyzer Decoding



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Packet List